

Figure 1 is a block diagram of a message authentication code (MAC) system. The system consists of two main components: a **Sender** (110) and a **Receiver** (120), both enclosed in dashed boxes.

**Sender (110):** This component takes a **message** (130) as input. It also receives an **Authentication Tag** (140) from the Receiver. The message (130) and the authentication tag (140) are combined to form a **MAC** (150), which is represented as a box containing **message** and **tag**. The MAC (150) is then sent to the Receiver.

**Receiver (120):** This component receives the **MAC** (150) and the **message** (130). The message (130) is input to an **Authentication Tag Computation** block (122). The MAC (150) is also input to this block. The block outputs a **tag'** (140') and a **comparison result** (140'') indicating whether the received tag (140') matches the computed tag (140').

FIG. 1

DOT 20" 09072960

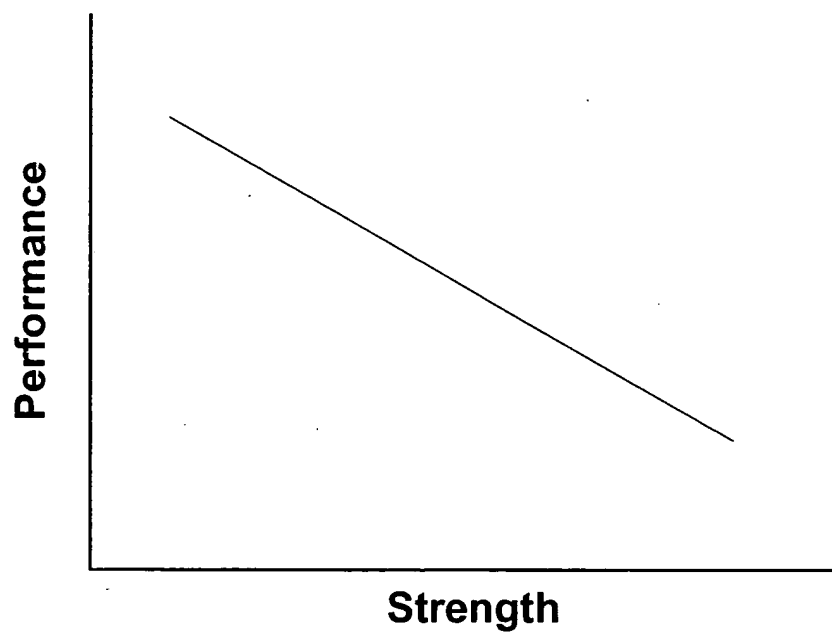


FIG. 2

300

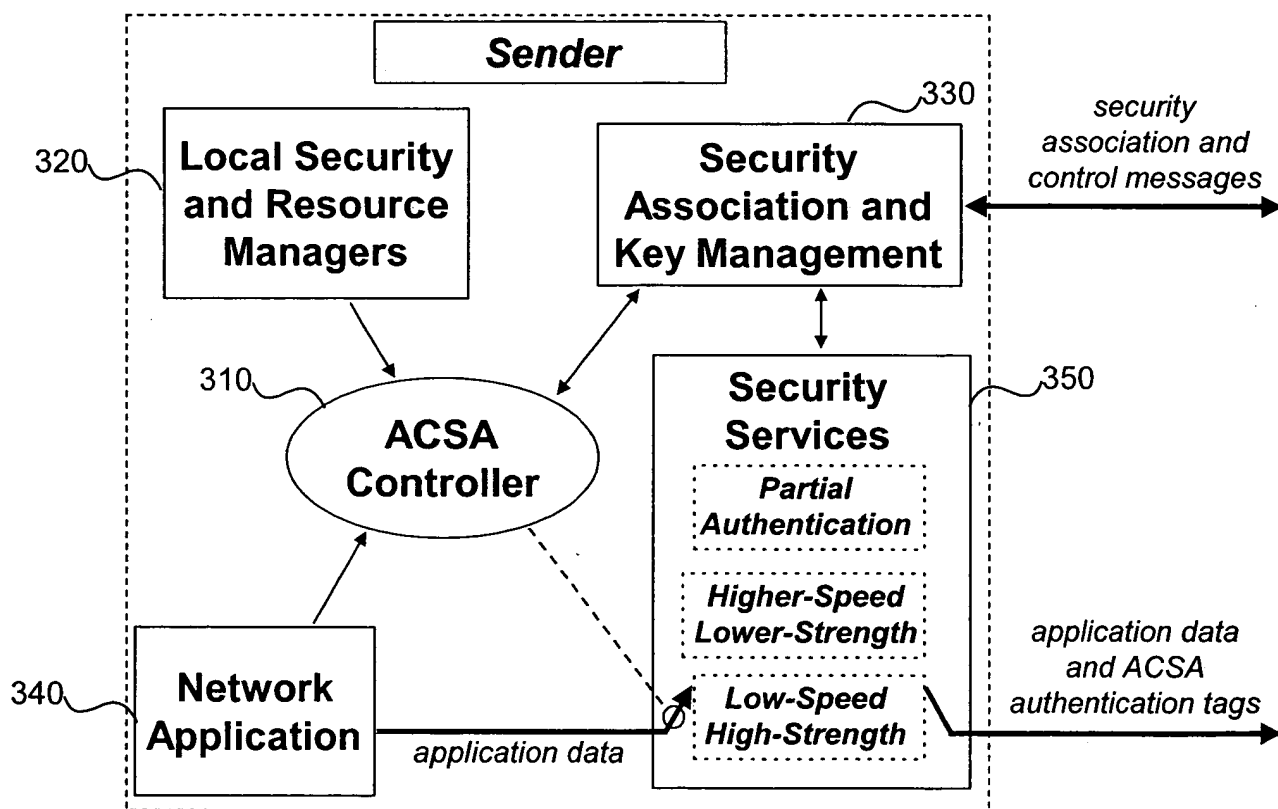


FIG. 3

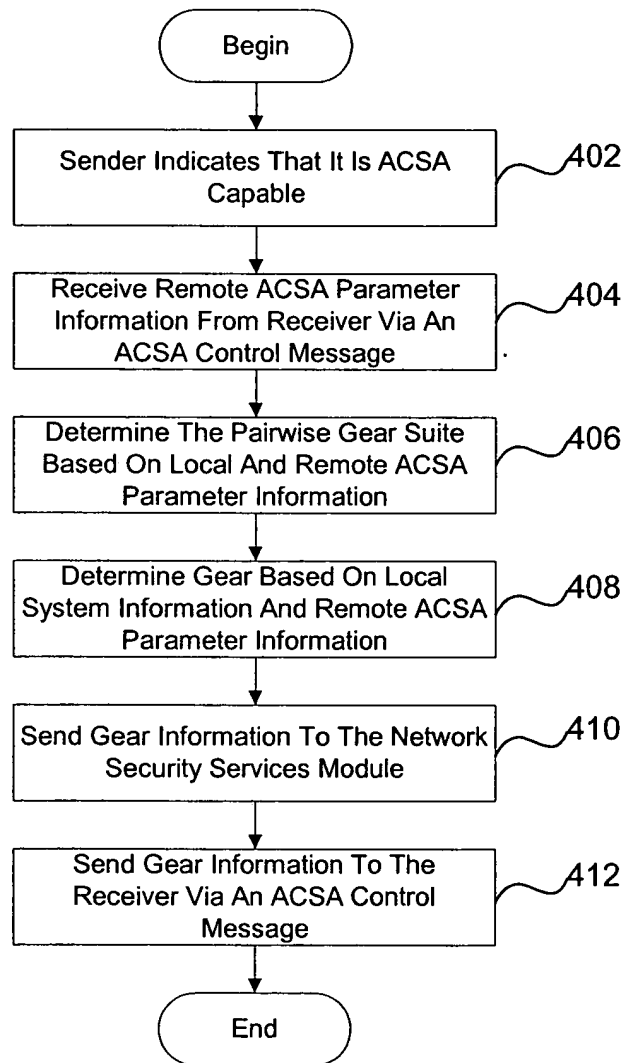


FIG. 4

09621060-072100

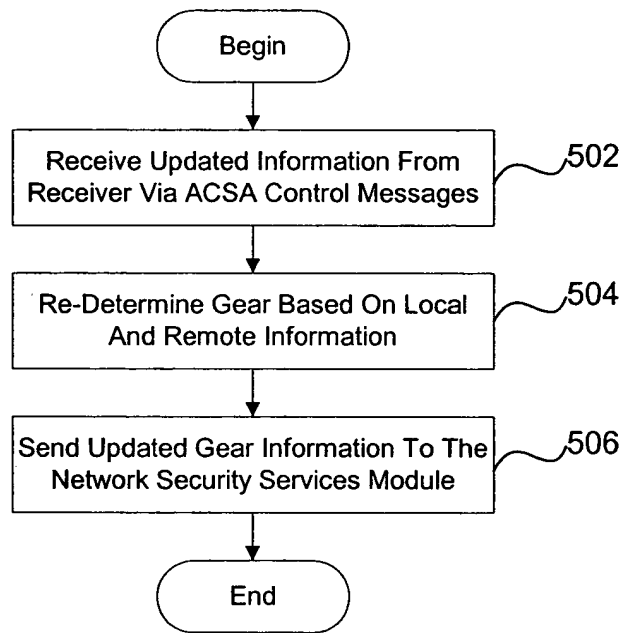


FIG. 5

00T20"050T2960

Receiver Processor Utilization				
Sender Processor Utilization		Too Heavily Loaded	Near Desired CPU Load	Lightly Loaded
	Too Heavily Loaded	switch to less computationally intensive gear	switch to less computationally intensive gear	switch to less computationally intensive gear
	Near Desired CPU Load	switch to less computationally intensive gear	maintain current gear	maintain current gear
	Lightly Loaded	switch to less computationally intensive gear	maintain current gear	switch to more secure (more computationally intensive) gear

FIG. 6

09621060-072100

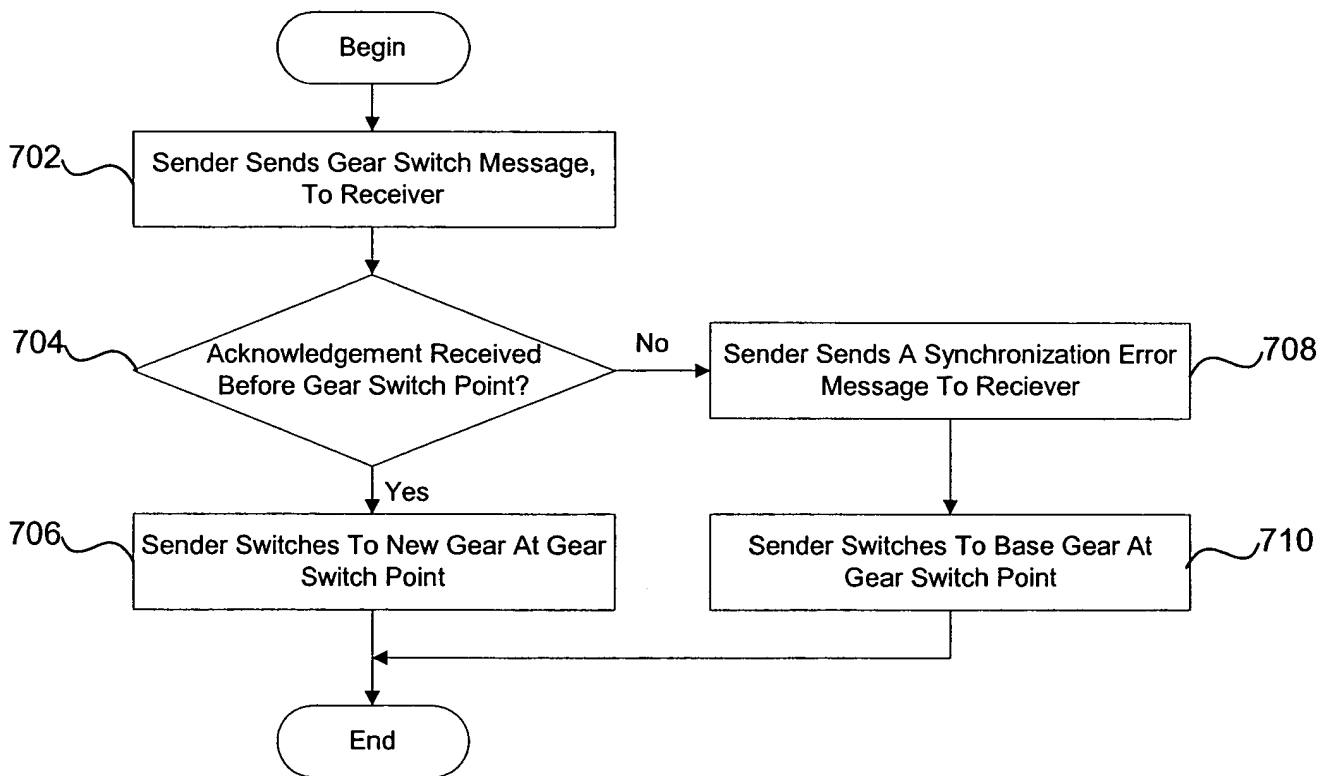


FIG. 7A

09621060-072100

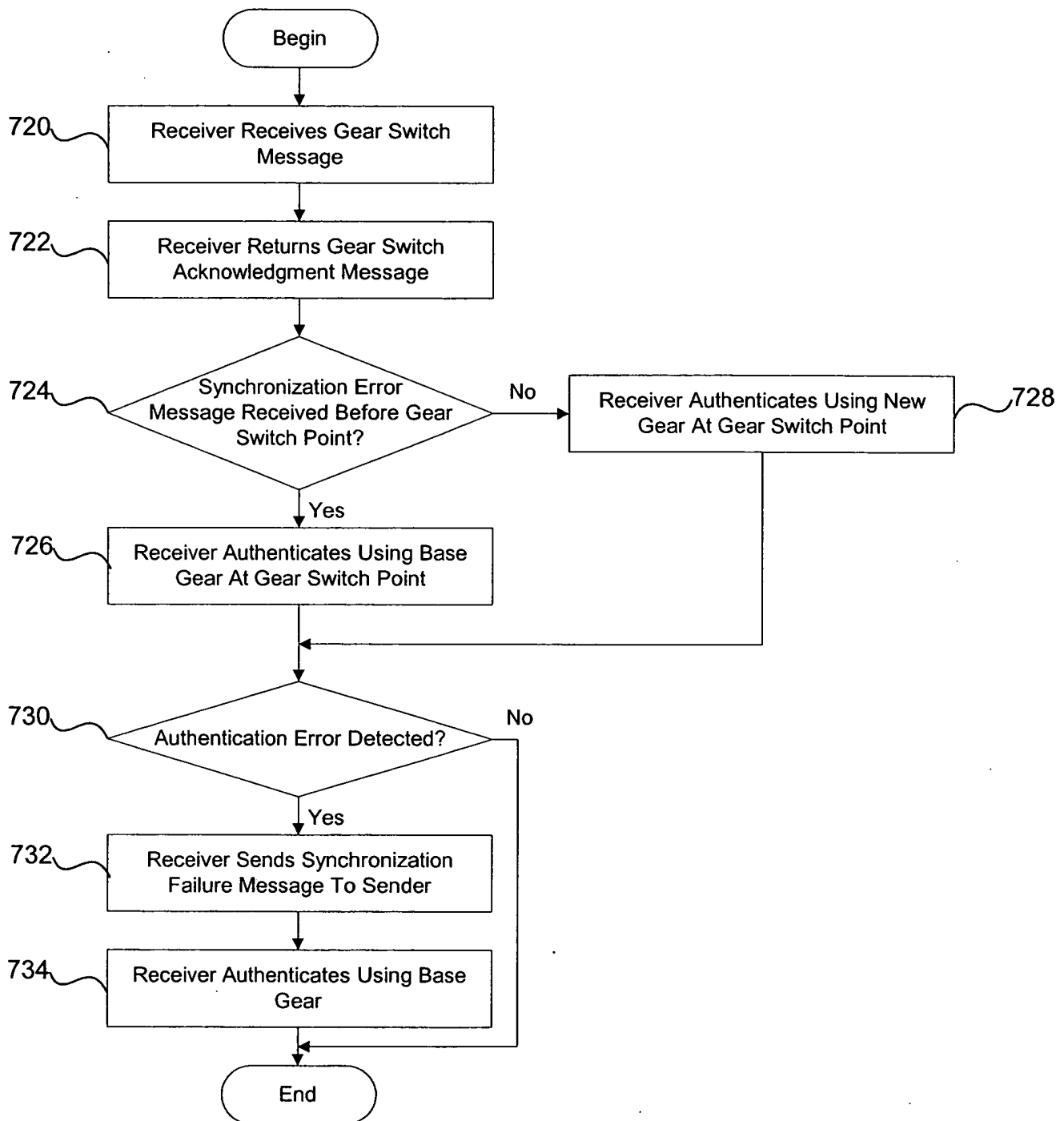


FIG. 7B





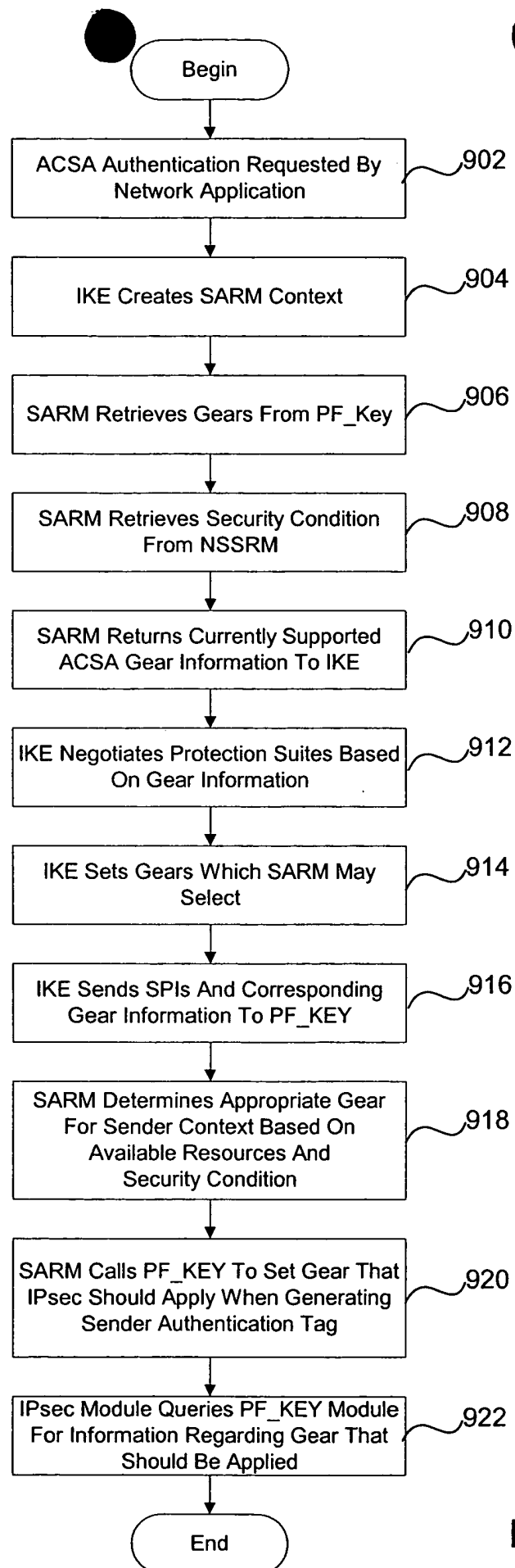


FIG. 9

00221050.072100

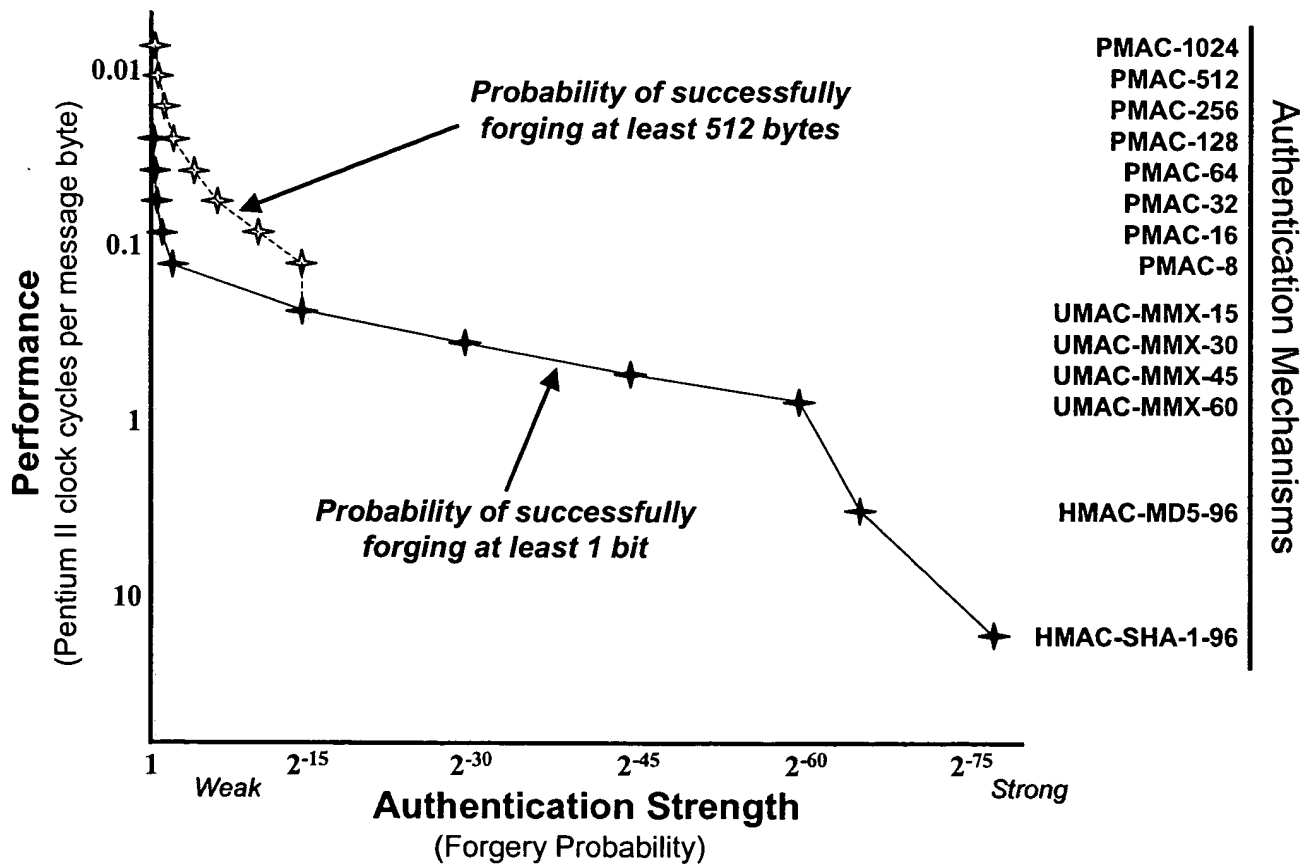


FIG. 10

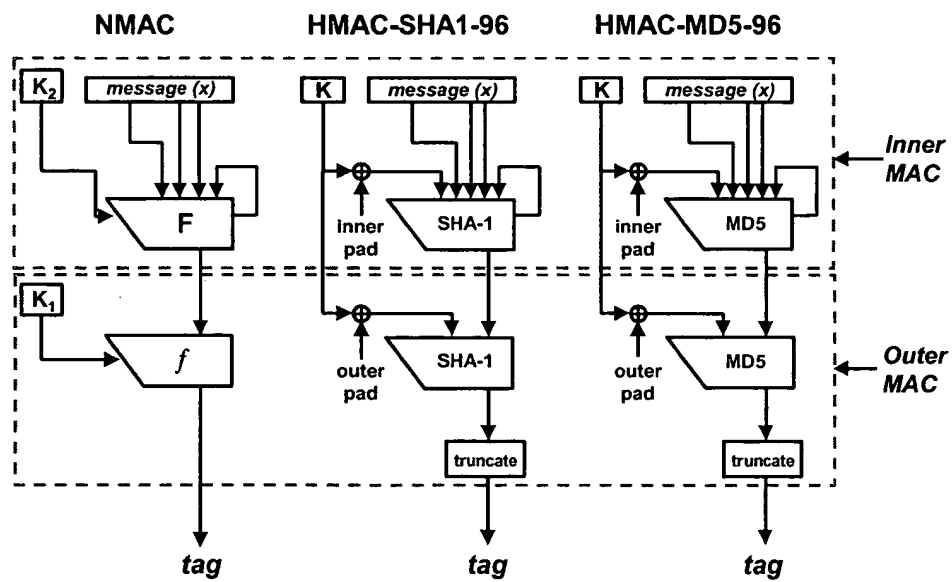


FIG. 11

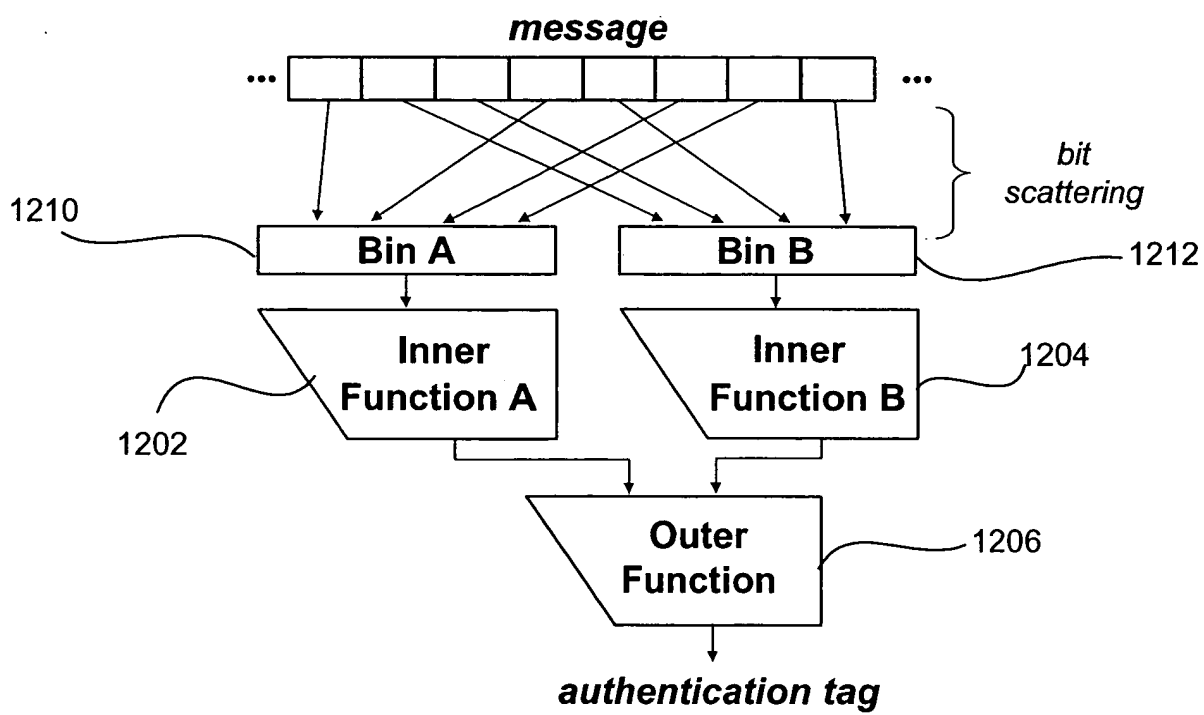


FIG. 12

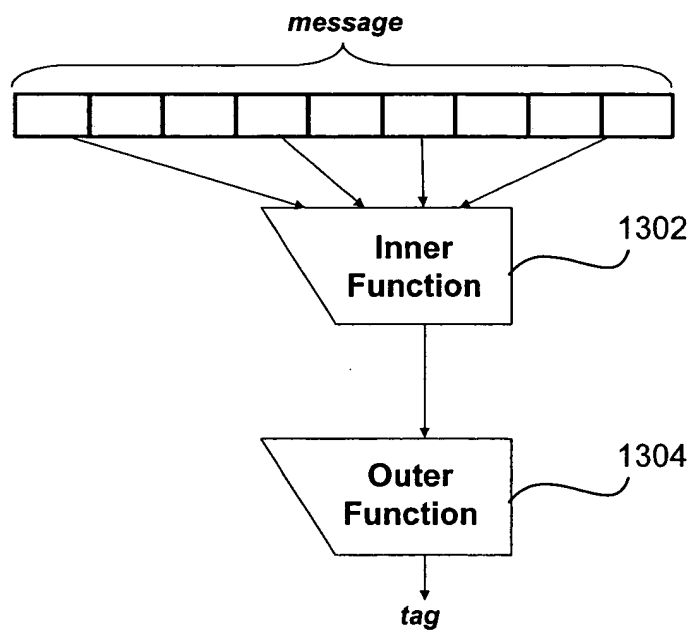


FIG. 13

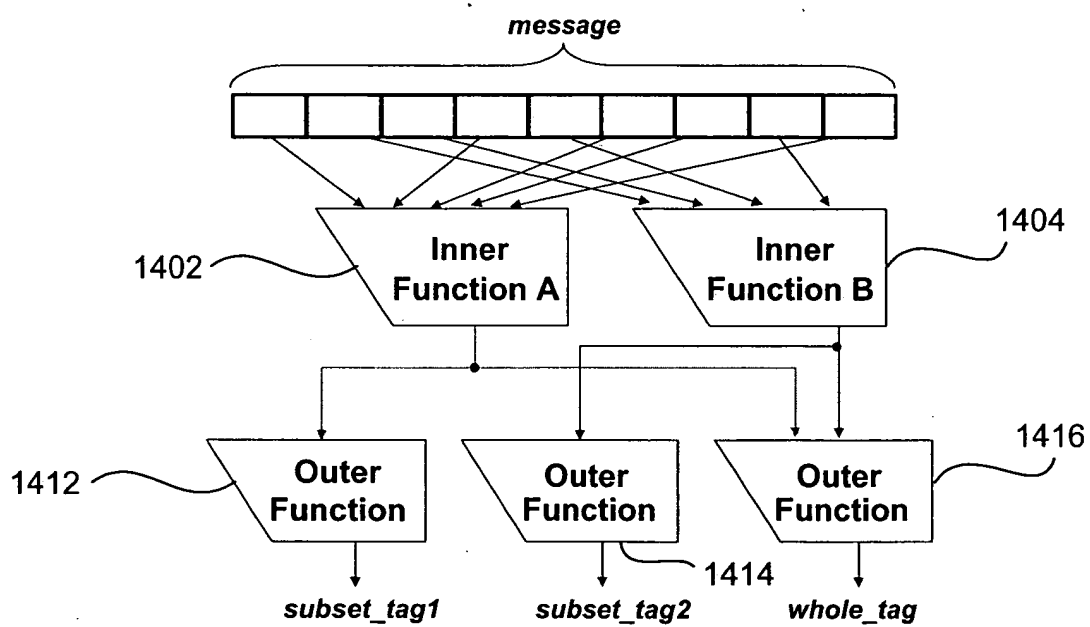


FIG. 14

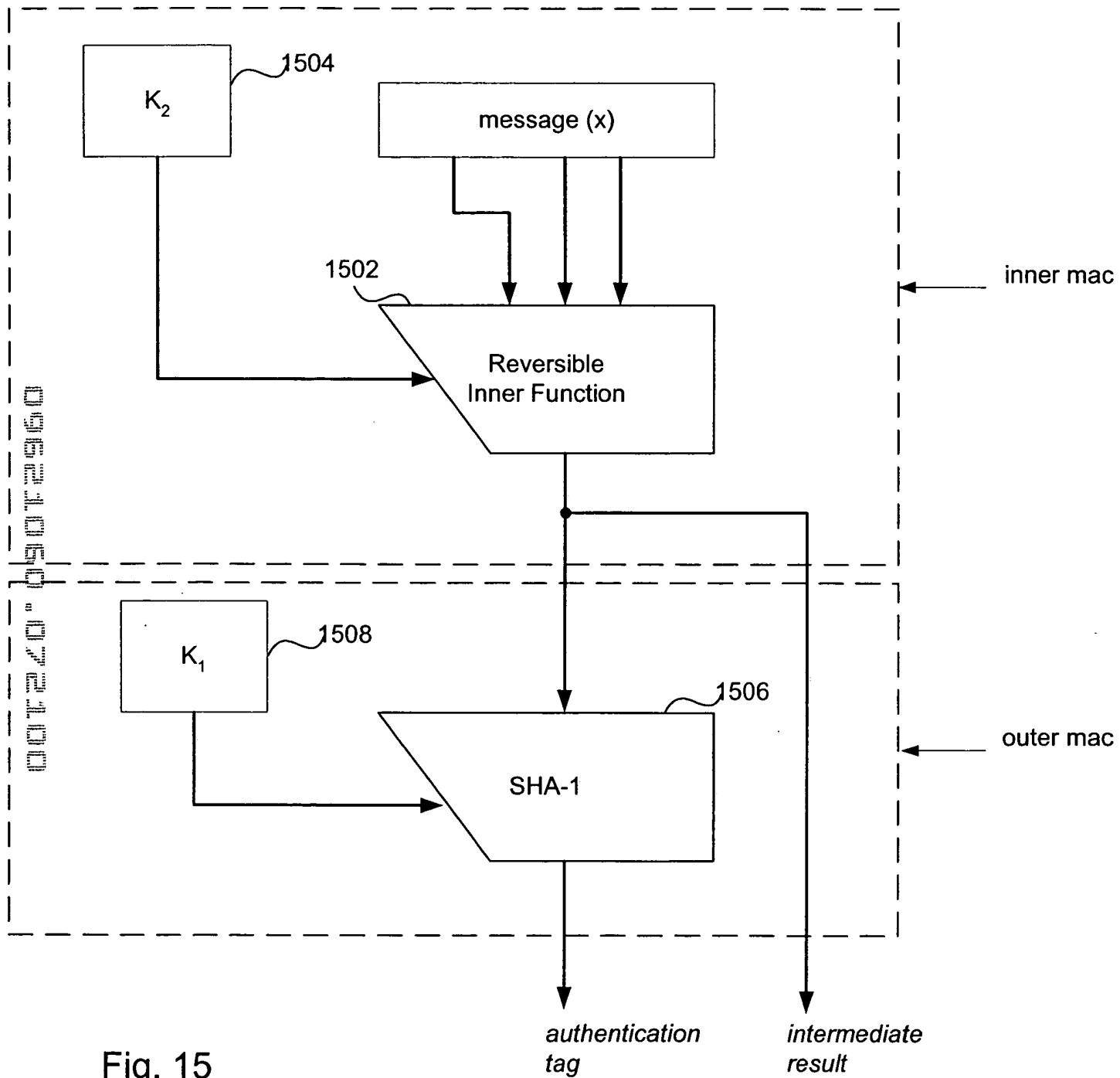


Fig. 15



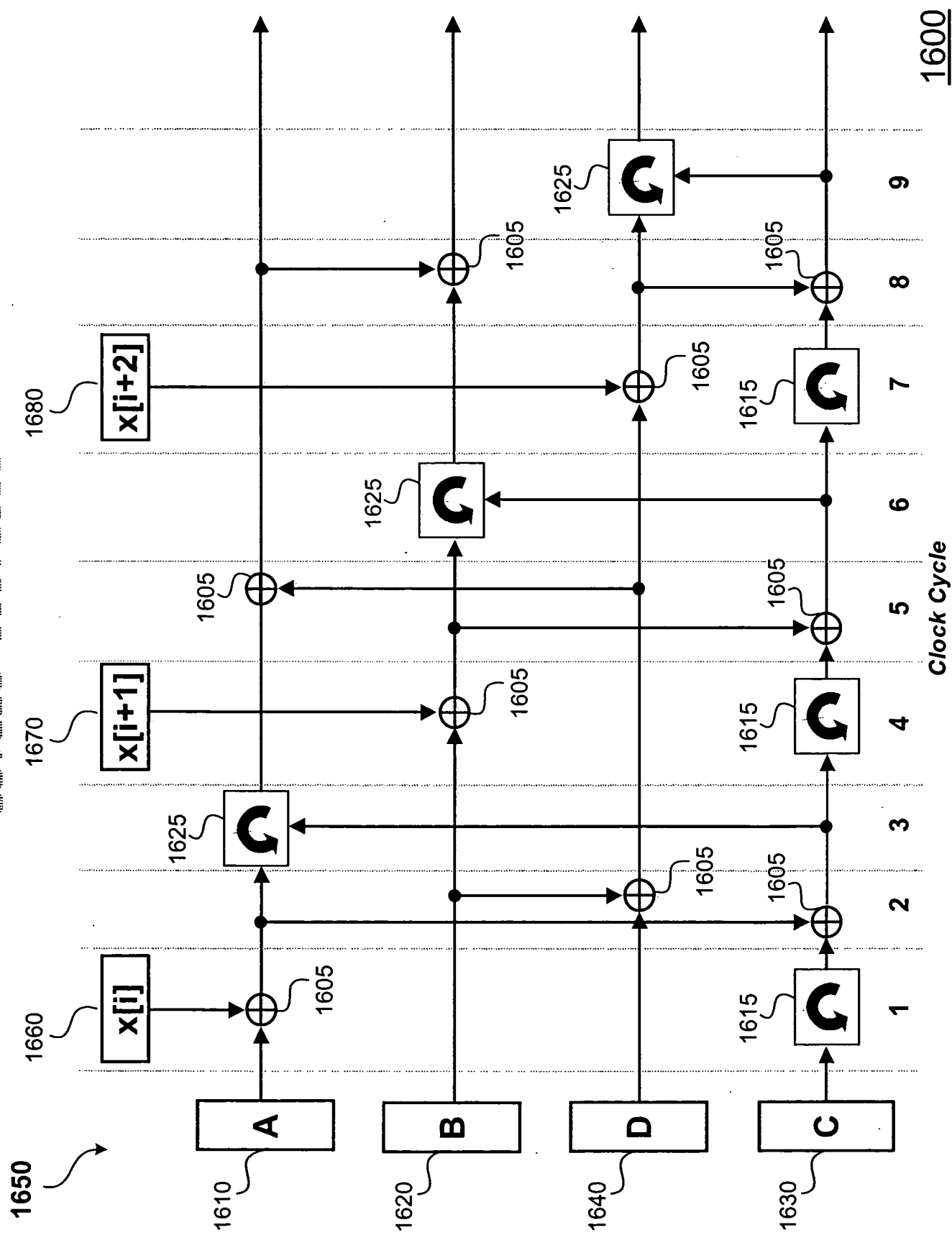


FIG. 16

09521060-02100

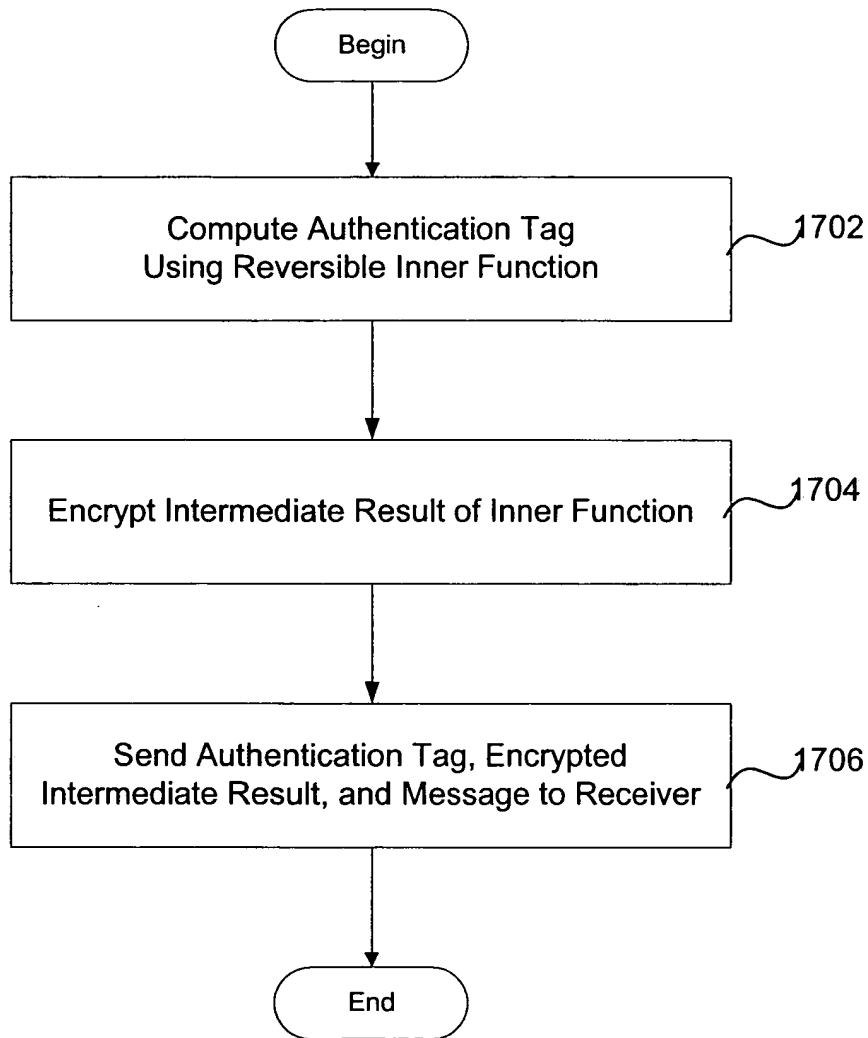


FIG. 17A

